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APPLICATION N	J. 1	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/040,223		01/02/2002	Eric Sprangle	42390.P13414	2904
8791	7590	09/08/2004		EXAMINER	
		LOFF TAYLOR & . OULEVARD	KIM, KENNETH S		
SEVENTH FLOOR				ART UNIT	PAPER NUMBER
LOS ANO	LOS ANGELES, CA 90025-1030			2111	
				DATE MAILED: 09/08/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.



	Application No.	Applicant(s)					
	10/040,223	SPRANGLE ET AL.					
Office Action Summary	Examiner	Art Unit					
	Kenneth S KIM	2111					
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with th	e correspondence address					
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a replevation of the propers of the properties of the propertie	136(a). In no event, however, may a reply b oly within the statutory minimum of thirty (30) will apply and will expire SIX (6) MONTHS f e, cause the application to become ABANDO	e timely filed days will be considered timely. from the mailing date of this communication. DNED (35 U.S.C. § 133).					
Status							
1) Responsive to communication(s) filed on <u>02</u> J	lanuary 2002.						
2a) This action is FINAL . 2b) ⊠ Thi							
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) <u>1-25</u> is/are rejected. 7) ☐ Claim(s) is/are objected to.	Claim(s) 1-25 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. Claim(s) is/are allowed. Claim(s) 1-25 is/are rejected. KENNETH'S. KIM						
8) Claim(s) are subject to restriction and/	or election requirement.						
Application Papers							
9) The specification is objected to by the Examiner.							
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the E							
Priority under 35 U.S.C. § 119							
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
Attachment(s)							
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date 	4) Interview Summ Paper No(s)/Ma 5) Notice of Inform 6) Other:						

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1. Claims 1-25 are presented for examination.

2. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

- 3. Claims 1-25 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.
- (a) Claims 1, 10, 13, 16, and 23, it is not clear what the source and destination are in reference to.
- (b) Claims 5, 10, 13, 20, and 23, there is no recitation of scheduling of an instruction for re-scheduling to make any sense.
- (c) Claims 3 and 18, it is not clear what is the function of the checker unit.
- 4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.
- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

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5. Claims 1, 2, 7-9, 16, 17, and 22 are rejected under 35 U.S.C. 102(b) as being anticipated by Papworth et al, U.S. Patent No. 5,452,426.

Papworth et al teaches the invention as claimed in claim 1 including a processor comprising:

- (a) at least one source location (originating location; col. 2, line 63; col. 9, line 9), each source location comprising a validity bit to indicate validity of data in the at least one source location,
- (b) a data validity circuit (90, 92, 94, 96, 38) coupled to the at least one source location to determine the validity of the data in the at least one source location, said data validity circuit to indicate the validity of the data, by writing a validity bit in a destination location (final location; reservation station entries; col. 10, line 15) based upon the validity bit in the at least one source location (transferred), and

further teaches as in claims 2 and 7-9,

- (c) wherein writing a validity bit in a destination location comprises writing a bit in a destination register to indicate validity of data in the destination location (valid flag; col. 10, line 15) claim 2,
- (d) wherein the data validity circuit comprises any one of an AND gate and an OR gate (integrated circuits comprise AND gates and OR gates) claim 7,
- (e) wherein the source location or the destination location is any one of a register, cache, and permanent memory (44) claims 8 and 9.

The processor claims 16, 17, and 22 are equivalently rejected based on the same reason.

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6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Fetterman taught a method of using source valid bit.

Sodani et al taught a method of using result valid bit.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kenneth S KIM whose telephone number is (703) 305-9693. The examiner can normally be reached on M-F (8:30-17:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark Rinehart can be reached on (703) 305-4815. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

September 3, 2004

PRIMARY EXAMINER